

AMENDMENTS TO THE SPECIFICATION

At page 8 please replace the paragraph commencing at line 10 with the following amended paragraph:

In the voltage supply circuit 120, the drain voltage of the PMOS transistor Q8 in the current mirror circuit 140 is provided to the gate of the PMOS transistor Q13. And, the external voltage (EV_{cc}) is provided to the source of the PMOS transistor Q13. The source of the PMOS transistor Q13 is connected to ~~the a back~~ gate of the PMOS transistor Q13. The drain of the PMOS transistor Q13 is connected to a set of NMOS transistors, for example, three NMOS transistors Q14-Q16. In particular, the drain of the PMOS transistor Q13 is connected to the drain and gate of the NMOS transistor Q14, the drain and gate of the NMOS transistor Q15 are connected to the source of the NMOS transistor Q14, the drain and gate of the NMOS transistor Q16 are connected to the source of the NMOS transistor Q15, and the source of the NMOS transistor Q16 is connected to the ground voltage.